

MEASURING LOGARITHMIC SIGNAL CONVERTER FOR MAGNETIC TRACKING SYSTEMS

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Abstract

The work deals with the problem of signal conversion in magnetic tracking systems. Magnetic tracking systems are a novel development trend of navigation sensors within the concepts of the Internet of Things and virtual and augmented reality. In contrast to optical tracking systems, magnetic ones do not suffer from occlusions. In comparison with tracking systems built upon inertial sensors, they are not susceptible to bias drift and provide better accuracy. Magnetic tracking technology is based on calculating the position of objects upon the dynamic measurement of the reference magnetic field vectors. The reference magnetic fields are formed by arrays of actuator coils in the low-frequency electromagnetic radiation spectrum. Those who implement a magnetic tracking system have to ensure noise-immune measurements of signals coming from sensor coils in a wide dynamic measurement range. The range changes from microvolts for distances of several meters in couples “actuator-sensor” to hundreds of millivolts in the case if the distances in “actuator-sensor” couples reduce to several centimeters. Thus, one requires signal converters able to provide highly noise-immune measurements in a dynamic measurement range covering six orders of magnitude. The work presents the results of development, simulation, and investigation into a signal converter for magnetic tracking systems, whose novelty consists in combining the methods of logarithmic amplification and synchronous demodulation of the output signals of the sensor coils. The main nodes of the developed signal converter are a control unit, a logarithmic amplifier, a synchronous demodulator, a low-pass filter, an actuator driver and an analog-to-digital converter. Voltage logarithm compression has been performed upon volt-ampere characteristics of semiconductor $p-n$ junctions. The synchronous demodulator provides a high level of selection of the useful signal out of electromagnetic noise. The results presented in this paper are part of our complex research work related to the development of the Magnetic Tracking System Integrated Development Environment (MTS-IDE). The latter is being developed by a team of scholars within different projects and is aimed at enhancing the efficacy of parametric optimization and synthesizing firmware of embedded systems implementing integrated magnetic tracking sensors. Simulation and experiments have shown that the dynamic range of noise-immune signal measurement using the developed converter covers six orders of magnitude, from $1E-6$ V to 1 V. Investigation into functionality were conducted by oscillograph methods. The characteristics of the proposed solution were measured by the above-mentioned MTS-IDE. The obtained results are of key importance for further improvement of magnetic tracking systems, particularly, for their noise-immune measurement volume expansion.

Keywords

Spatial Navigation, Magnetic Tracking, Virtual Reality Devices, Analog Front End, Signal Compression, Logarithmic Converter

1. Introduction

Magnetic tracking systems are a novel development trend of sensors for object navigation that conforms with Virtual Reality (VR) and Augmented Reality (AR) concepts [1]. Magnetic tracking technology relies on calculating the spatial position of an object being tracked based on the measurement of the reference magnetic field vectors in the low-frequency electromagnetic radiation spectrum. Hence, the technology is also referred to as Electromagnetic Tracking (EMT) [2].

Measurements are carried out using coils that form a system of coupled actuators and sensors. Actuator coils induce AC (Alternative Current) magnetic fields whereas sensor coils generate an electromotive force (AC voltage) caused by the fields. The informative signals that characterize the mutual location in couples “actuator – sensor” are described by functional dependencies whose main arguments are the magnetic coupling coefficient in the couple and the quantities to be measured. The latter is the distance between the coils and the angle between them. The spatial position is calculated upon mathematical models of the distribution of the formed magnetic fields. The models are synthesized upon theoretical assumptions and experimental results.

A substantial advantage of magnetic tracking systems over optical ones is the fact that they do not require a direct line of sight. In contrast to inertial tracking systems, which utilize inertial measurement units, magnetic tracking systems do not suffer from bias drift intrinsic for accelerometers [3] and gyroscopes [4] and are able to provide high-precision measurements of the coordinates of the sensors in a coordinate frame formed by the actuator array.

Modern solutions for magnetic tracking include AR IoT, a universal framework that conforms with the AR and Internet of Things (IoT) concepts [5]; data fusion in medical imaging devices [6]; surgical instruments navigation in medical equipment [7]; biomedical engineering technologies including those for monitoring orofacial kinematics [8]; flexible robot mechatronics tools [9], etc. Magnetic tracking systems can be implemented upon specialized or unified coils [10]. Mainly universality of magnetic tracking systems is provided by 3D (3-Dimension) small-size coils [11]. A combination of magnetic and inertial tracking is presented in [12, 13].

2. Shortcomings

When designing a magnetic tracking system one has to ensure noise-immune measurement of signals coming from sensor coils in a wide dynamic measurement range (typically, voltages are measured). The range changes from microvolts for distances of several meters in couples “actuator-sensor” to hundreds of millivolts in the case if the distances in “actuator-sensor” couples reduce to several centimeters. Therefore, one requires signal converters able to provide highly noise-immune measurements in a dynamic measurement range covering six orders of magnitude. Thus, the solution to the above-mentioned important problem of highly-noise immune measurements in the stated range would give rise to enhancement of operating characteristics of magnetic tracking systems.

3. Goal of the Study

The work is aimed at development, simulation and experimental study of a signal converter for magnetic tracking systems, which would provide highly noise-immune signal measurements in a measurement range covering six orders of magnitude and thus allow expansion of measurement volumes without loss of accuracy in measuring coordinates.

4. Measuring Logarithmic Signal Converter

Research results in the field of the magnetic tracking are related to position and orientation estimates in magnetic tracking systems [14], development of magnetic tracking systems for near-field scanning [15], calibration of tracking systems for dynamic field distortion compensation [16], positional accuracy assessment and error modeling for magnetic tracking systems [17], etc.

The measurement principle in magnetic tracking systems consists in calculation of the object spatial position. Precisely, arrays of coordinates and angles of sensors in some given coordinate system are to be found. In the origin of this coordinate frame, the actuator forms a low-frequency magnetic field. Usually one-, two- or three-dimensional (3D) small-size coils serve as sensors and actuators. Approximately, the maximum value of the output voltage V_{OUT} of the sensor coil is described by the cubic dependency on distance R between the sensor and actuator, $V_{OUT} \propto R^{-3}$ [10].

The signal converter proposed in this work is part of complex research dedicated to the development of the Magnetic Tracking System Integrated Development Environment (MTS-IDE). The research has been carried out by our team in a series of commercial projects, which somewhat restricts disclosure of details. MTS-IDE is intended for enhancing the efficacy of parametric optimization and synthesizing firmware of embedded systems implementing magnetic tracking sensors.

The work presents the results of development, modeling, and investigation into a measuring signal converter for magnetic tracking systems. The novelty of the proposed converter consists in a combination of logarithmic amplification methods and synchronous demodulation of the output voltage of sensor coils. Fig. 1 shows the block diagram of the signal converter whose informative signal is the magnetic coupling coefficient between the sensor coil and actuator one. The main blocks of the scheme are Control Unit, Log Amplifier, Synchro Detector, LF (low frequency) Filter, Actuator Driver, ADC (analog-to-digital converter).

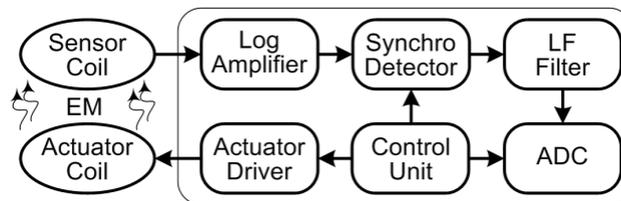


Fig. 1. The block diagram of the measuring signal converter for magnetic tracking systems

Briefly, the signal converter operation can be described as follows. The Control Unit forms a driving signal, which is AC voltage in frequency band f_{MT} ranging from 10 Hz to 500 kHz. As is corroborated by literature analysis [18] and our own study, there exist criteria for the selection of the optimal frequency f_{MT0} of the driving signal. So, at frequencies $f_{MT} < 1$ kHz the magnetic coupling coefficient in couples “actuator – sensor” is rather small, which makes it necessary to increase the diameter of coils and their number of turns. On the contrary, at frequencies $f_{MT} > 1$ kHz the

reference magnetic field is getting increasingly distorted by metal items in vicinity, which leads to accuracy deterioration. The Actuator Driver forms the Actuator Coil current using the driving signal.

The Log Amplifier amplifies the AC voltage formed on the Sensor Coil and thus provides compression of its dynamic range. Compression is done by taking the logarithm of the voltage, which exploits the corresponding properties of the volt-ampere characteristics of p-n junctions. The schematic of the logarithmic amplifier shown in Fig. 2 incorporates operational amplifier OA₁ whose negative-feedback circuit is formed by diodes D₁ and D₂. The volt-ampere characteristics of p-n junctions of these diodes are described by exponential dependencies. Therefore, voltage V_{OUT} at the output of the amplifier depends on the logarithm of the input current, $I_{IN} = V_{IN}/R_1$, where V_{IN} is the voltage on sensor coil SC; R₁ is the resistance of the resistor in the amplifier input circuit. Resistor R₂ is used for stabilizing the amplifier below some threshold current value, which is defined by parasitic currents in p-n junctions and on a printed board surface. The resistance of R₂ is typically chosen within the range 10⁵...10⁷ Ohm.

Let us analyze the parameters of diodes before designing and verifying the mathematical model of the logarithmic amplifier. We chose diodes of the BAS16 family for the implementation of our signal converter. They are constructed using the SMD (Surface Mount Device) technology and possess a wide current range, high speed, and small-size.

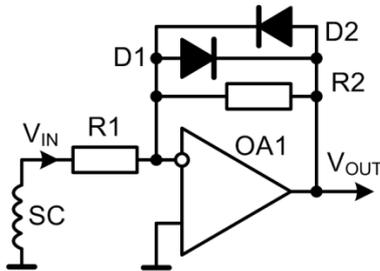


Fig. 2. The schematic of the logarithmic amplifier

Experimental data and the approximated volt-ampere characteristic of the diode BAS16 are shown in Fig. 3. One can observe three distinctive regions. The main region relates to currents within the range from 10⁻⁶ A to 10⁻² A and is approximated by a logarithmic dependency of voltage on current – Ln approx. Such an approximation is broken if the current is less than the lower threshold caused by parasitic currents or exceeds the upper threshold caused by parasitic resistances of the diode structure.

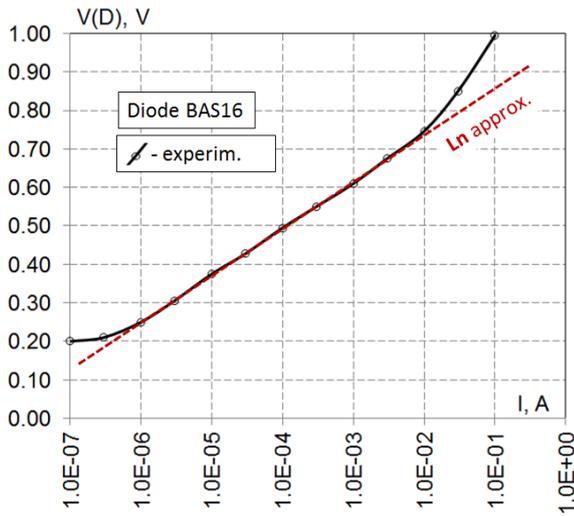


Fig. 3. Experimental data and approximation of the volt-ampere characteristic of the BAS16 diode

Another substantial block of the signal converter is Synchro Detector. It detects the output voltage of the logarithmic amplifier and thus provides an efficient selection of the useful signal out of the noise. This property is achieved by switching the signal polarity in accordance with phase synchronizing pulses which are time-controlled by the driving signal of the Control Unit. The schematic of the synchronous demodulator depicted in Fig. 4 incorporates operational amplifier OA₁, whose input circuits are switched by analog two-position switch SW₁ and whose negative-feedback circuits are formed by resistors R₁ and R₂.

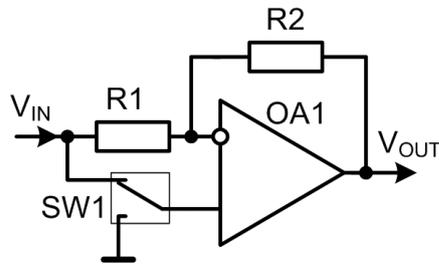


Fig. 4. The schematic of the synchronous demodulator

When the switch is in its upper position (in accordance with the schematic), the non-inverting input of OA₁ is connected with the circuit of input voltage V_{IN}. Assuming that there is no parasitic voltage in the open switch circuit and there is no offset voltage in the operational amplifier, one can claim that no current flows through resistor R₁ and, consequently, no voltage drops on the resistor. Therefore, output voltage V_{OUT} of the schematic is equal to its input voltage, V_{OUT} = V_{IN}, which implies the input voltage repeater mode. When the switch is in its lower position (in accordance with the schematic), the non-inverting input of OA₁ is connected with the common signal point. The negative-feedback circuit holds zero voltage (the common point potential) at the inverting input. Hence, the current flowing through resistor R₁ is I_{IN} = V_{IN}/R₁. In this mode the output voltage of the circuit is V_{OUT} = -R₂·V_{IN}/ R₁, which means the voltage inversion mode, V_{OUT} = -V_{IN}, provided that R₂ = R₁.

Other blocks – LF Filter, Actuator Driver, and ADC – are typical components of most measuring converters. Their parameters and operation are not crucial from the standpoint of the novelty of this work and therefore are not considered here.

5. Signal Converter Verification

Simulation of the measuring converter was carried out on the basis of universal and specialized SPICE models of operational amplifiers, diodes, multiplexers and other components in MicroCap. At the first stage, we studied volt-ampere characteristics of diodes for discrete temperature values ranging from 0°C to 100°C (Fig. 5). Such a study assumes that the SPICE model of diodes included in the logarithmic amplifier should be verified and specified in accordance with the selected diode type. The main result of this investigation stage is that the actual diode parameters and the temperature coefficients of the diode SPICE model have been brought to an agreement.

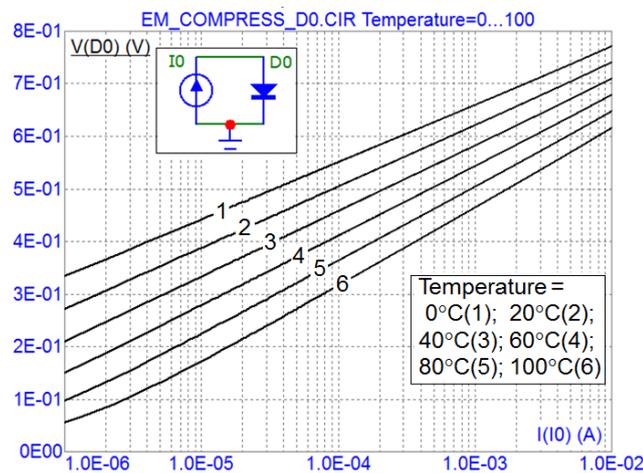


Fig. 5. The dependencies of voltage V(D0) at the diode on current I(I0) for the following temperature values: Temperature = 0°C (1); 20°C (2); 40°C (4); 60°C (4); 80°C (5); 100°C (6)

At the second stage, we carried out an investigation aimed at the adjustment of the diode SPICE model parameters taking into account the discrepancies between the diode characteristic and the theoretical mathematical dependencies for p-n junctions. For the most part, such discrepancies are attributed to the fact that the diode volt-ampere characteristic is influenced by the emission of charge carriers in the p-n barrier and parasitic resistance of its semiconductor structure. The SPICE model defines emission of charge carriers by coefficient N (Emission Coefficient), whereas parasitic resistance is set by resistor R_s (Series Resistance). Fig. 6 shows the simulation results that take into consideration the above-mentioned reasons for discrepancies between the theory and practice for injection coefficients N = 1.0 (1), 1.5 (2), 2.0 (3). Fig. 7 depicts the simulation results for discrete values of parasitic diode resistance R_s = 0 (1), 10 Ohm (2), 20 Ohm (3), 30 Ohm (4), 40 Ohm (5). The obtained modeling results

demonstrate discrepancies between the actual volt-ampere characteristic and its nominal logarithmic dependency. The results are in good agreement with the experiments shown in Fig. 3.

At the last stage, we performed integrated simulation dedicated primarily to transient analysis. With this purpose, we synthesized an equivalent circuit of the SPICE model that incorporated both synchro detector and logarithmic amplifier blocks. The transient analysis results for the measuring converter are shown in Fig. 8. The time diagrams of the input signal and its synchronous demodulation were obtained for an input sine wave of frequency 1 kHz and discrete amplitude values E-6 V (1); 1E-5 V (2); 1E-4 V (3); 1E-3 V (4); 1E-2 V (5); 1E-1 V (6); 1 V (7), with and without taking the logarithm. The obtained results back up the requirements set to synchronous signal demodulation and its highly efficient logarithmic compression.

SPICE simulation results of the proposed measuring converter and the parameters were experimentally verified using a prototype. The converter functionality was studied using oscillographic methods (Fig. 9). Its parameters were measured using the above-mentioned MTS-IDE – Magnetic Tracking System Integrated Development Environment (Fig. 10).

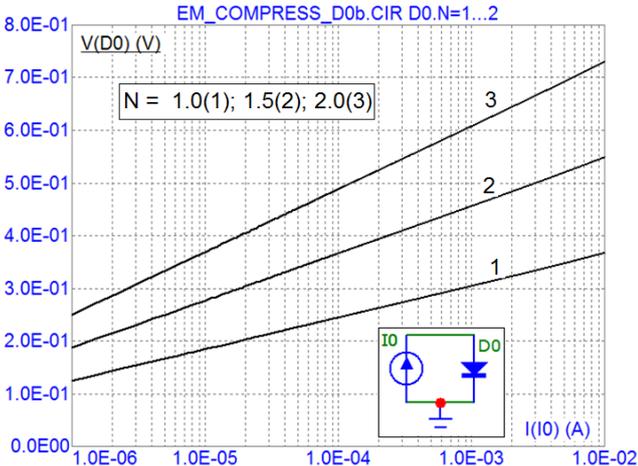


Fig. 6. The dependencies of voltage $V(D0)$ at the diode on the current $I(I0)$ for the following injection coefficients: $N = 1.0$ (1); 1.5 (2); 2.0 (3)

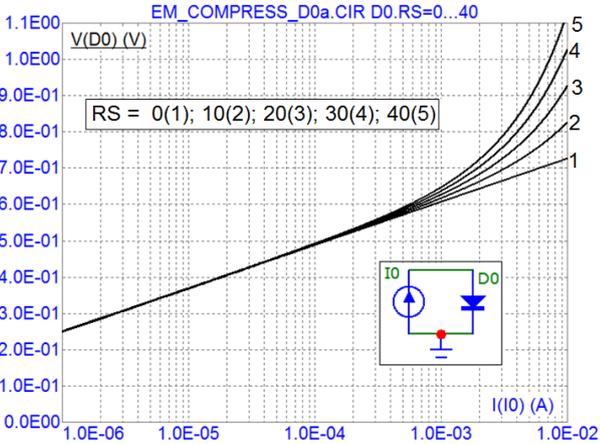


Fig. 7. The dependencies of voltage $V(D0)$ at the diode on the current $I(I0)$ for the following parasitic diode resistance values: $RS = 0$ (1); 10 Ohm (2); 20 Ohm (3); 30 Ohm (4); 40 Ohm (5)

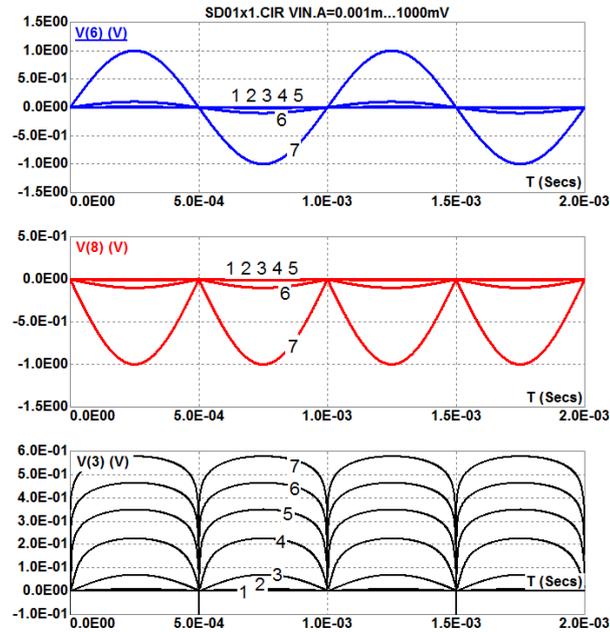


Fig. 8. The time diagrams of the input signal (on the top) and the results of synchronous demodulation without (in the middle) and with (at the bottom) taking the logarithm

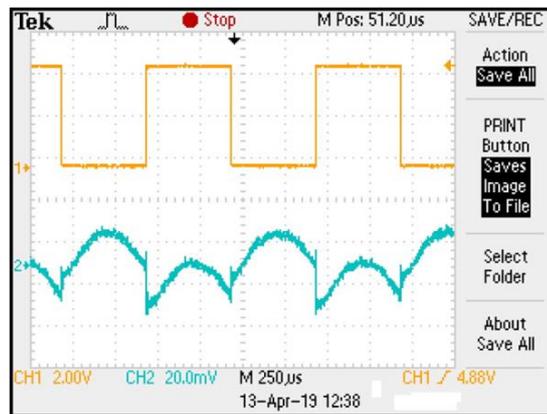


Fig. 9. Sample oscillograms of sync pulses (on the top) and signals at the output of the synchronous demodulator (at the bottom)

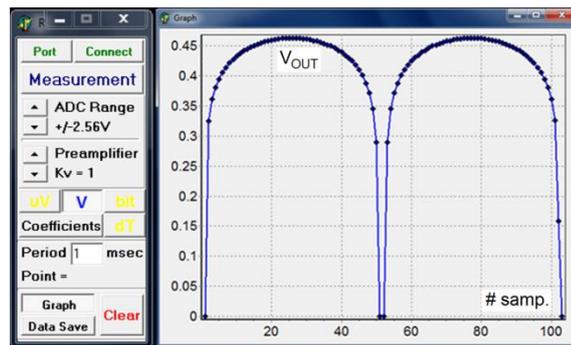


Fig. 10. The result of an experimental investigation into signals at the converter output using MTS-IDE

The analog-to-digital converter of MTS-IDE is implemented upon the micro converter ADuC834 with a word length of 24 bits. Our experiments have proven that the dynamic range of noise-immune signal conversion covers six orders of magnitude, from $1\text{E-}6\text{ V}$ to 1 V . The obtained results are of key importance for further enhancement of magnetic tracking systems in general and expansion of their measurement volumes while preserving noise-immunity in particular.

6. Conclusion

The work presents the results of developing a logarithmic measuring converter for magnetic tracking systems. It has been shown that one of the problems crucial for further enhancement of magnetic tracking systems is a noise-immune measurement of signals from sensor coils in a wide measurement range. More precisely, the measurement range from

several microvolts to hundreds of millivolts is required. The first values correspond to distances of several meters in couples “actuator – sensor”. The latter ones relate to the case when the distances reduce to several centimeters.

The novelty of the proposed measuring signal converter for magnetic tracking systems consists in a combination of logarithmic amplification methods and synchronous demodulation of the output voltage of sensor coils. Voltage logarithm compression has been performed upon volt-ampere characteristics of semiconductor p - n junctions. The synchronous demodulator provides an efficient selection of the useful signal from electromagnetic noise. Simulation and experiments have proved that the dynamic range of noise-immune signal measurement using the developed converter covers six orders of magnitude, from $1E-6$ V to 1 V. The characteristics of the proposed solution have been measured with the above-mentioned MTS-IDE. Its functionality was investigated with oscillograph methods. The obtained results are of key importance for further improvement of magnetic tracking systems, particularly for their noise-immune measurement volume expansion.

7. Conflict of Interest

The authors claim that there are no possible financial or other conflicts over the work.

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